



i.MX RT1160 Crossover MCU Dual-Core Arm® Cortex®-M7 and Cortex-M4

i.MX-RT1160

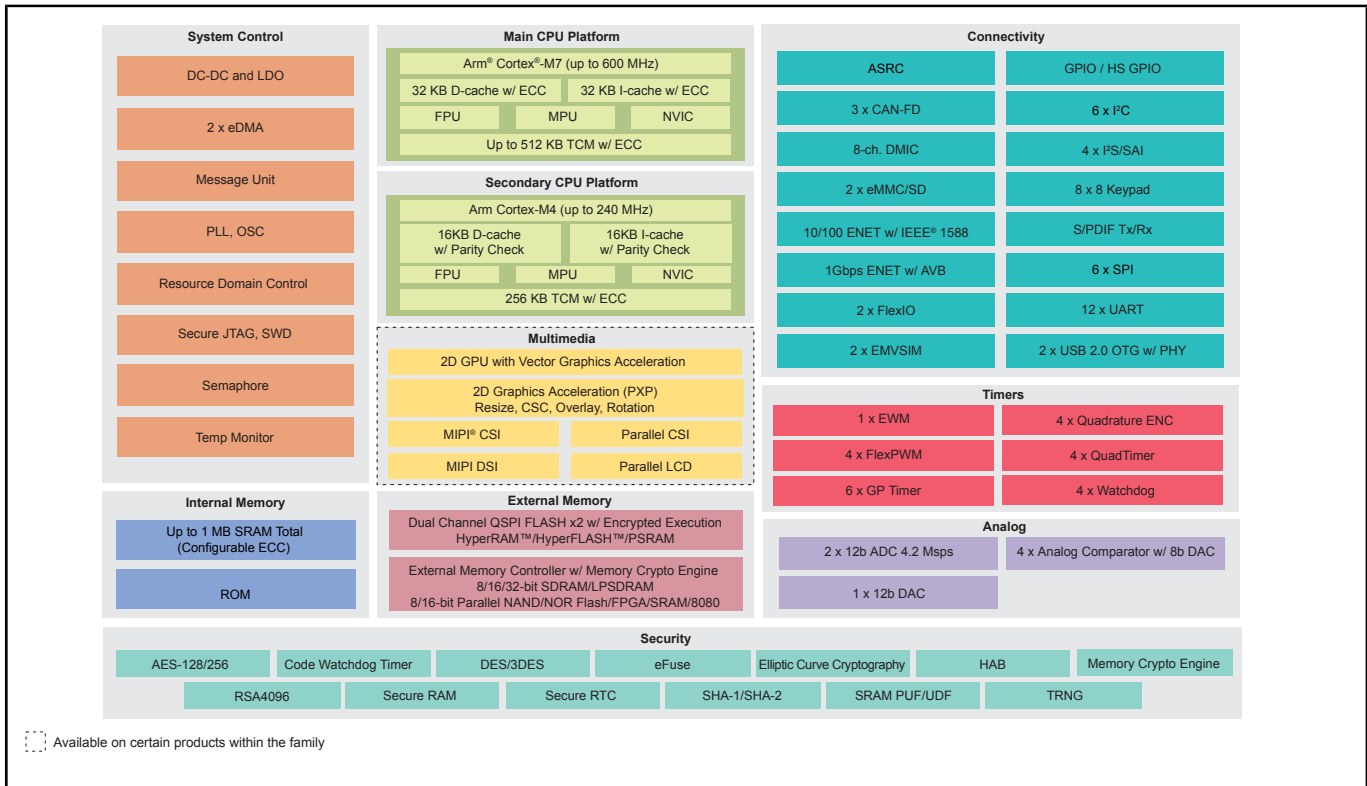
Last Updated: Apr 11, 2024

i.MX RT1160 Crossover MCUs are dual-core devices featuring an Arm® Cortex®-M7 and Cortex-M4 for real-time performance and high integration for Industrial and IoT applications.

The i.MX RT1160 CM7 operates at up to 600 MHz and the CM4 up to 240 MHz with 1 MB on-chip RAM. The family offers advanced 2D graphics, MIPI-CSI, MIPI-DSI, and various memory interfaces, a wide range of connectivity interfaces including 3x Ethernet up to Gbps with TSN/AVB, UART, SPI, I²C, USB and 3x CAN. The i.MX RT1160 provides advanced embedded security including secure boot and crypto engines.

The i.MX RT1160 family is supported by the [MCUXpresso ecosystem](#), which includes an SDK, a choice of IDEs and secure provisioning and configuration tools to enable rapid development.

i.MX RT1160 Crossover MCU Block Diagram



View additional information for [i.MX RT1160 Crossover MCU Dual-Core Arm® Cortex®-M7 and Cortex-M4](#).

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2024 NXP B.V.